

PREDICTIVELY CODED PICTURE

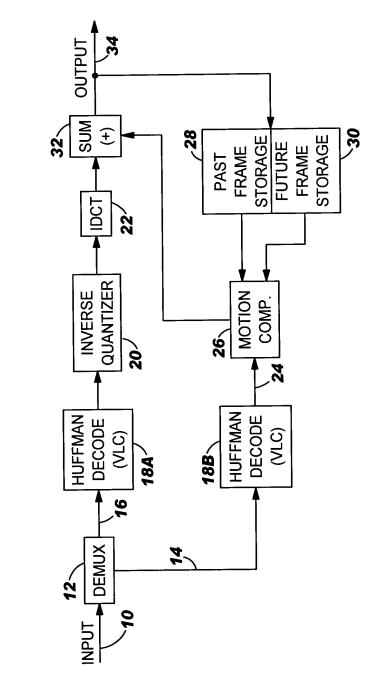


FIG. 2

FIG. 3

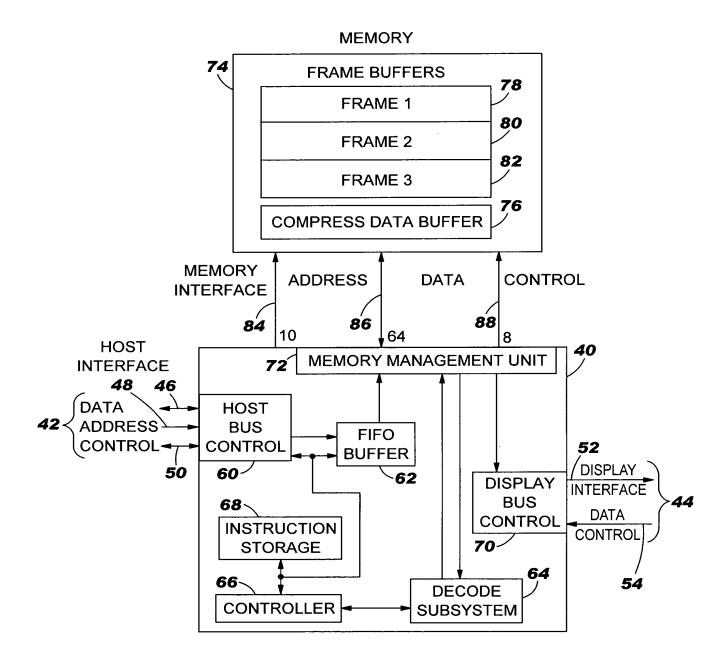
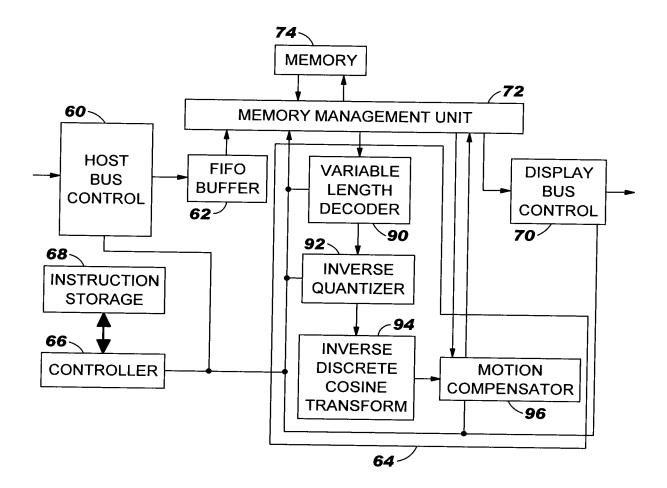
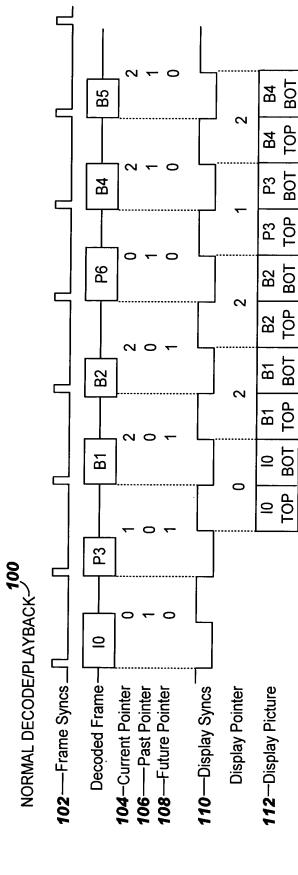


FIG. 4

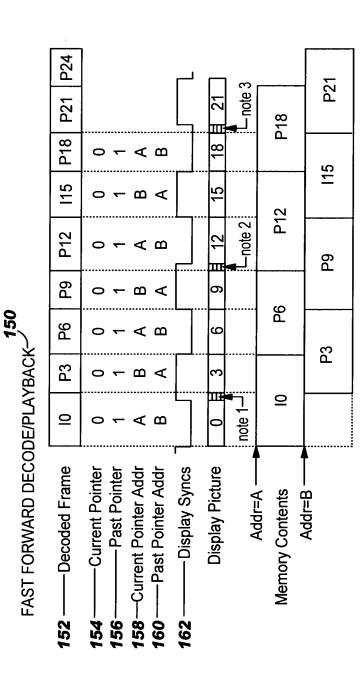




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FIG. 5

FIG. 6



Note 1 - Top portion is formed from picture 0 since picture 3 has not decoded yet

Note 2 - Bottom portion is formed by picture 6 since picture 12 has not decoded far enough yet

Note 3 - Bottom portion is taken from picture 15 since picture 21 has not decoded far enough yet

FIG. 7

	200	NORMAL MODE (HEX ADR)	LOW DELAY MODE (HEX ADR)
USER ORG		¬ 0	0
OSD ORG	USER DATA BUFFER	200	200
	OSD BUFFER		
206 –Y0 ORG - 7	BUFFER 0 LUMA	BA00	BA00
UV0 ORG	BUFFER 0 CHROMA	60000	60000
208 −Y1 ORG		8A300	BA00
204 UV1 ORG →	BUFFER 1 LUMA	DE000	60000
Y2	BUFFER 1 CHROMA	DE900	60000
	BUFFER 2 LUMA	108C00	BA00
UV2 ORG →	DUEEED & OUDOMA	- 15D200	60000
Y-SPILL ORG	BUFFER 2 CHROMA	187500	BA00
UV-SPILL ORG	SPILL BUFFER LUMA	192900	B7300
OV-OFILL ONG	SPILL BUFFER CHROMA	192900	B7 300
DATA ORG	COMPRESS DATA BUFFER	198300	CDB00